



DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

Device	V _{(BR)DSS}	R _{DS(ON) max}	I _{D MAX} T _A = +25°C
		25mΩ @ V _{GS} = 4.5V	6.9A
N-Channel	12V	$30m\Omega$ @ $V_{GS} = 2.5V$	6.3A
		$38m\Omega$ @ $V_{GS} = 1.8V$	5.5A

Description

This MOSFET has been designed to minimize the on-state resistance $(R_{DS(on)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Load Switch
- Power Management Functions
- Portable Power Adaptors

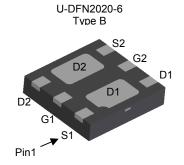
Features

- Low On-Resistance
- Low Input Capacitance
- Low Profile, 0.6mm Max Height
- ESD protected gate.
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

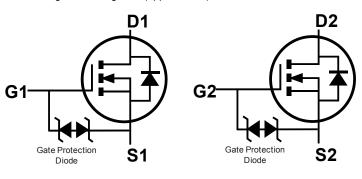
Mechanical Data

- Case: U-DFN2020-6 Type B
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 ⁶⁴
- Terminals Connections: See Diagram Below
- Weight: 0.0065 grams (approximate)





Bottom View



N-CHANNEL MOSFET

N-CHANNEL MOSFET

Internal Schematic

Ordering Information (Note 4)

Part Number	Case	Packaging	
DMN1025UFDB-7	U-DFN2020-6 Type B	3000/Tape & Reel	
DMN1025UFDB-13	U-DFN2020-6 Type B	10000/Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/quality/product_compliance_definitions/.

Marking Information



NB = Product Type Marking Code YM = Date Code Marking Y = Year (ex: A = 2013) M = Month (ex: 9 = September)

Date Code Key

Date Code Ney												
Year	201	2	2013		2014	20	15	2016		2017	2	2018
Code	Z		Α		В	(2	D		E		F
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Drain-Source Voltage		V_{DSS}	12	V	
Gate-Source Voltage		V_{GSS}	±10	V	
Continuous Drain Current (Note 5) / - 4.5/			I _D	6.9 5.5	А
Continuous Drain Current (Note 5) V _{GS} = 4.5V			I _D	8.8 7.0	А
Maximum Continuous Body Diode Forward Curre	ent (Note 5)		Is	1	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1	%)	I _{DM}	35	Α	
Avalanche Current (Note 6) L = 0.1mH	alanche Current (Note 6) L = 0.1mH		I _{AS}	9.8	A
Avalanche Energy (Note 6) L = 0.1mH		E _{AS}	4.8	mJ	

Thermal Characteristics

Characteristic		Symbol	Value	Units	
Total Power Dissipation (Note 5)	Steady State	Б	1.7	W	
Total Power Dissipation (Note 5)	t < 5s	P_{D}	2.9		
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	П	71		
Thermal Resistance, Junction to Ambient (Note 5)	t < 5s	$R_{\theta JA}$	43	°C/W	
Thermal Resistance, Junction to Case (Note 5)	$R_{ heta JC}$	13			
Operating and Storage Temperature Range		$T_{J_i} T_{STG}$	-55 to 150	°C	

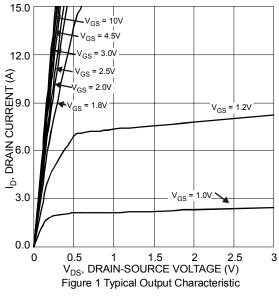
Electrical Characteristics N-CHANNEL (@T_A = +25°C, unless otherwise specified.)

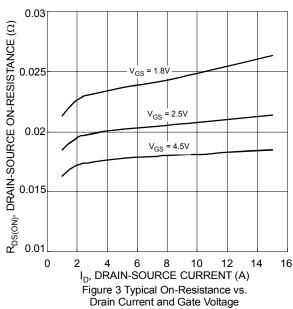
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	12	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}		1	1.0	μΑ	$V_{DS} = 12V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	0.4		1	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
	` '	_	18	25		$V_{GS} = 4.5V, I_D = 5.2A$
Static Drain-Source On-Resistance	R _{DS} (ON)	_	20	30	mΩ	$V_{GS} = 2.5V, I_D = 4.8A$
	1	_	25	38		V _{GS} = 1.8V, I _D = 2.5A
Diode Forward Voltage	V _{SD}	_	0.7	1.2	V	V _{GS} = 0V, I _S = 5.4A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	_	917	_	pF	., ., ., .,
Output Capacitance	Coss	_	120	_	pF	$V_{DS} = 6V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	102	_	pF	1 - 1.0WHZ
Gate Resistance	Rg	_	11.4	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (V _{GS} = 4.5V)		_	12.6	_	nC	
Total Gate Charge (V _{GS} = 8V)	- Q _g		23.1	_	nC	10/ 10/ 1
Gate-Source Charge	Q _{gs}	_	1.3	_	nC	$V_{DS} = 10V, I_D = 6.8A$
Gate-Drain Charge	Q _{qd}		1.6	_	nC	1
Turn-On Delay Time	t _{D(on)}	_	3.0	_	ns	
Turn-On Rise Time	t _r		9.3	_	ns	$V_{DD} = 6V, V_{GS} = 4.5V,$
Turn-Off Delay Time	t _{D(off)}		17.2	_	ns	$R_L = 1.1\Omega$, $R_G = 1\Omega$
Turn-Off Fall Time	t _f		2.8	_	ns	1
Body Diode Reverse Recovery Time	trr	_	6.8	_	nS	$I_S = 5.4A$, $dI/dt = 100A/\mu s$
Body Diode Reverse Recovery Charge	Qrr	_	1.1	_	nC	I _S = 5.4A, dI/dt = 100A/µs

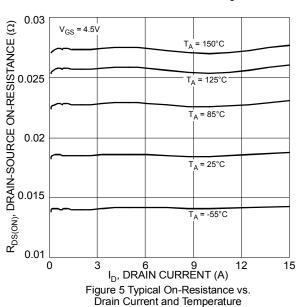
5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. 6. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep T_J = +25°C 7. Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.

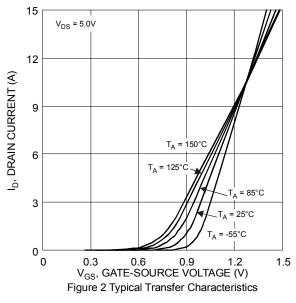


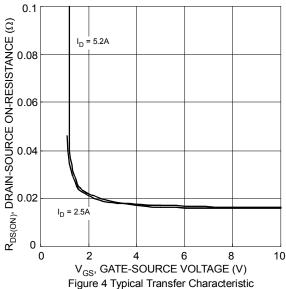












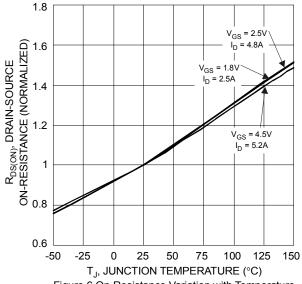


Figure 6 On-Resistance Variation with Temperature



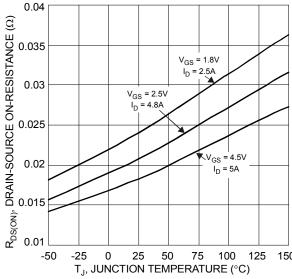
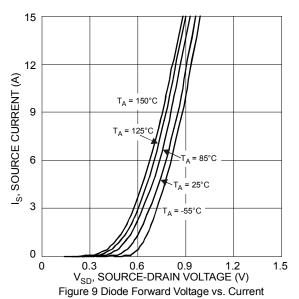
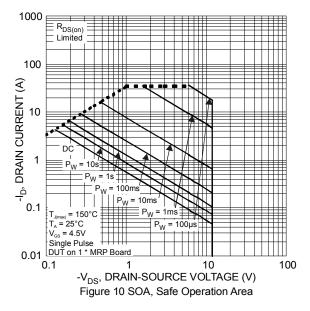


Figure 7 On-Resistance Variation with Temperature



1 $V_{\text{GS(th)}}$, GATE THRESHOLD VOLTAGE (V) 8.0 0.6 $I_D = 1mA$ = 250µA 0.4 0.2 50 75 100 125 -50 25 150 T_J, JUNCTION TEMPERATURE (°C)

Figure 8 Gate Threshold Variation vs. Ambient Temperature

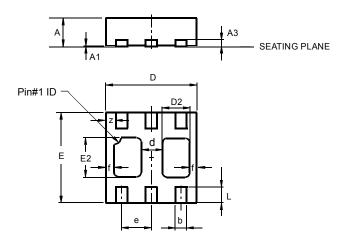


r(t), TRANSIENT THERMAL RESISTANCE 10 10 10 10 D = 0.1 D = 0.05 D = 0.01 $R_{\theta JA}(t) = r(t) * R_{\theta JA}$ $R_{\theta JA} = 178^{\circ}C/W$ Single Pulse Duty Cycle, D = t1/t20.001 0.00001 0.0001 0.001 0.01 0.1 10 100 1000 t1, PULSE DURATION TIMES (sec) Figure 11 Transient Thermal Resistance



Package Outline Dimensions

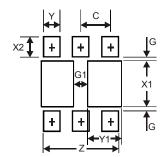
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



U-DFN2020-6									
	Type B								
Dim	Min Max Typ								
Α	0.545	0.605	0.575						
A1	0	0.05	0.02						
A3	_	_	0.13						
b	0.20	0.30	0.25						
D	1.95	2.075	2.00						
d	_	_	0.45						
D2	0.50	0.70	0.60						
е	_	_	0.65						
Е	1.95	2.075	2.00						
E2	0.90	1.10	1.00						
f		_	0.15						
L	0.25	0.35	0.30						
z		_	0.225						
All	All Dimensions in mm								

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	1.67
G	0.20
G1	0.40
X1	1.0
X2	0.45
Y	0.37
Y1	0.70
С	0.65



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2014, Diodes Incorporated

www.diodes.com